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## TRANSMITTAL FORM

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Total Number of Pages in This Submission

Application Number 10/777,327

Filing Date February 12, 2004

First Named Inventor Ngjel G. Herron

Art Unit 2133

Examiner Name David Ton

Attorney Docket Number X-1545 US

### ENCLOSURES (Check all that apply)

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Remarks

### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

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Date	January 9, 2006	Reg. No.	37,652

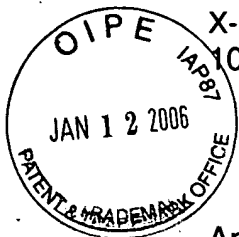
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X-1545 US  
10/777,327

PATENT  
Conf. No.: 4920

IN THE UNITED STATES PATENT OFFICE

Applicants: Nigel G. Herron et al.  
Assignee: Xilinx, Inc.  
Title: Testing a Programmable Logic Device with Embedded Fixed Logic Using a Scan Chain

Serial No.:	10/777,327	File Date:	02-12-04
Examiner:	David Ton	Art Unit:	2133
Docket No.:	X-1545 US	Conf. No.:	4920

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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the Patent Office on October 4, 2005, please replace/substitute the following claims as indicated.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 7 of this paper.